## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## REISSUE APPLICATION

U.S. Patent No.:

5,528,482

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Applicant:

Allen Frank Rozman

Title:

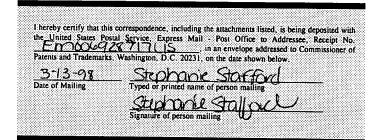
Low Loss Synchronous Rectifier for Application to Clamped-Mode Power

Converters

Commissioner of Patents and Trademarks Washington, D. C. 20231

Box: Reissue

Sir:



## INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO-1449. Copies of the listed references are submitted herewith.

| U.S. Patent No. | <u>Date</u> | <u>Inventor</u> |
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## Other References

"A Mosfet Resonant Synchronous Rectifier for High-Frequency DC/DC Converters" by Wojciech A. Tabisz, Fred C. Lee and Dan Y. Chen; 1990 IEEE; pp. 769-779

<sup>&</sup>quot;Constant Frequency, Forward Converter with Resonant Transition" by Ionel Dan Jitaru; HFPC; June 1991 Proceedings; pp. 282-293

"Practical Application of Mosfet Synchronous Rectifiers" by James Blanc; 1991 IEEE; pp. 495-501

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- "Study of the Applicability of Self-Driven Synchronous Rectification to Resonant Topologies" by J.A. Cobos, J. Sebastian, J. Uceda, E. de la Cruz, and J.M. Gras; 1992 IEEE; pp. 933-940
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- "Principles of Solid-State Power Conversion" by Ralph E. Tarter; 1985; pp. 544-547

"Buck Quasi-Resonant Converter Operating at Constant Frequency: Analysis, Design and Experimentation" by Ivo Barbi, J.C. Bolacell, D.C. Martins, F.B. Libano; 1989 IEEE; pp. 873-880

The Applicant sees the following patents as being more material to the patentability of the claims of the present application.

| U.S. Patent No. | <u>Date</u> | <u>Inventor</u>       |
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| 5,434,768       | 07/18/95    | Jitaru, et al.        |

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

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